EE 434 Lecture 24

Bipolar Small Signal Device Models

Quiz 16

What is a "binning model" and what is the purpose of using "binning models"?

And the number is 1 ⁸ ⁷ 5 3 ⁶ 9 4 2



Quiz 16

What is a "binning model" and what is the purpose of using "binning models"?

Solution:

A binning model is actually a set of models whereby the model derived for dimensions close to those of a specific device is used rather than using the same model for each device (the functional form of most binning models does not change, simply the parameters in the model)

A good binning model will more closely predict the actual characteristics of a device than a model that does not change with device dimensions.

Review from Last Time Models for Computer Simulation



Review from Last Time

Concept in modeling is to partition model into two parts, one that characterizes the technology and the other that characterizes the geometric aspects of a device

Technology part of the model common to all devices in a process (Level 1, BSIM4, PSP models – over 100 paramaters in BSIM 4 model)

 $\label{eq:weight} \begin{array}{l} Geometric \ information \ unique \ to \ each \ device \\ \{W,L,N_{RD}, \ N_{RS}, \ A_D,A_S,P_D,P_S\}, \ (default \ values \ used \ in \ not \ specified) \end{array}$

Models based upon physical principles but emperically modified to either simplfy model or improve validity

Geometric description may not be unique

Anticipated parasitics often included at schematic level for design prior to layout

Hierarchy used in models

Bipolar Models



Recall:

Small-Signal Model



- Small signal circuit model is linear (and unique at a Q-point)
- Small signal equivalent circuits are not unique



Small-Signal Model





Small signal model is that which represents the relationship between the small signal voltages and the small signal currents

For small signals, this relationship should be linear

Recall:

Small-Signal Model $i_{1} = g_{1}(v_{1}, v_{2}, v_{3})$ $i_{2} = g_{2}(v_{1}, v_{2}, v_{3})$ $i_{1} = g_{3}(v_{1}, v_{2}, v_{3})$ 3 Terminal Device $\mathbf{i}_1 = y_{11}\mathbf{v}_1 + y_{12}\mathbf{v}_2 + y_{13}\mathbf{v}_3$ $\mathbf{y}_{ij} = \frac{\partial \mathbf{f}_i (\mathbf{V}_1, \mathbf{V}_2, \mathbf{V}_3)}{\partial \mathbf{V}}$ $i_{2} = y_{21}v_{1} + y_{22}v_{2} + y_{23}v_{3}$ $i_{3} = y_{31}v_{1} + y_{32}v_{2} + y_{33}v_{3}$

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Small-Signal Model





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Recall:

Small-Signal Model



∛ y22

 V_2

 V_1



$$\mathbf{y}_{11} = \frac{\partial \mathbf{I}_{\mathsf{B}}}{\partial \mathbf{V}_{\mathsf{BE}}} \bigg|_{Q-PT} \stackrel{defn}{=} g_{\pi} \qquad \qquad \mathbf{y}_{21} = \frac{\partial \mathbf{I}_{\mathsf{C}}}{\partial \mathbf{V}_{\mathsf{BE}}} \bigg|_{Q-PT} \stackrel{defn}{=} g_{m}$$

$$\mathbf{y}_{12} = \frac{\partial \mathbf{I}_{\mathsf{B}}}{\partial \mathbf{V}_{\mathsf{CE}}} \bigg|_{Q-PT} \stackrel{defn}{=} ? \qquad \qquad \mathbf{y}_{22} = \frac{\partial \mathbf{I}_{\mathsf{C}}}{\partial \mathbf{V}_{\mathsf{CE}}} \bigg|_{Q-PT} \stackrel{defn}{=} g_{o}$$

Region of Operation for Small Signal Model :

Forward Active

$$\begin{aligned} y_{11} &= \frac{\partial I_B}{\partial V_{BE}} \bigg|_{Q-PT} = \frac{1}{V_t} \left(\frac{J_S A_E}{\beta} e^{\frac{V_{BE}}{V_t}} \right) \bigg|_{Q=PT} = \frac{I_{BQ}}{V_t} = \frac{I_{CQ}}{\beta V_t} \end{aligned}$$

$$\begin{aligned} y_{12} &= \frac{\partial I_B}{\partial V_{CE}} \bigg|_{Q-PT} = 0 \end{aligned}$$

$$\begin{aligned} y_{21} &= \frac{\partial I_C}{\partial V_{BE}} \bigg|_{Q-PT} = \frac{1}{V_t} \left(J_S A_E e^{\frac{V_{BE}}{V_t}} \right) \bigg|_{Q=PT} = \frac{I_{CQ}}{V_t} \end{aligned}$$

$$\begin{aligned} y_{22} &= \frac{\partial I_C}{\partial V_{CE}} \bigg|_{Q-PT} = \frac{1}{V_{AF}} \left[J_S A_E e^{\frac{V_{BE}}{V_t}} \bigg|_{Q-PT} \right]_{0} = \frac{I_{CQ}}{V_{AF}} \end{aligned}$$

"1" $I_{B} = \frac{J_{S}A_{E}}{\beta}e^{\frac{V_{BE}}{V_{t}}}$

"2"
$$I_{\rm C} = \beta I_{\rm B} \left(1 + \frac{V_{\rm CE}}{V_{\rm AF}} \right)$$



$$g_{m} = \frac{I_{CQ}}{V_{t}}$$
$$g_{\pi} = \frac{I_{CQ}}{\beta V_{t}}$$
$$g_{o} \cong \frac{I_{CQ}}{V_{AF}}$$





Alternate equivalent small signal model



Properties of the BJT

Alternate Equivalent Small Signal Model

- Relative magnitude of small signal parameters
 - Simplified small signal model





Alternate equivalent small signal model



Properties of the BJT

Alternate Equivalent Small Signal Model
 Magnitude of small signal parameters

Relative Magnitude of Small Signal Parameters



Often the go term can be neglected in the small signal model because it is so small

Relative Magnitude of Small Signal Parameters



Often the go term can be neglected in the small signal model because it is so small

Simplified small signal model



Comparison of BJT and MOSFET



The transconductance of the BJT is typically much larger than that of the MOSFET (and larger is better!)

This is due to the exponential rather than quadratic output/input relationship



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This is due to the exponential rather than quadratic output/input relationship

 $\mathsf{g}_{o}\cong rac{\mathsf{I}_{\mathsf{CQ}}}{\mathsf{V}_{\mathsf{AF}}}$

 $g_o = \lambda I_{DQ}$



The output conductances are comparable but that of the BJT is usually modestly smaller (and smaller is better!)

Comparison of MOSFET and BJTBJTMOSFET $g_{\pi} = \frac{I_{CQ}}{\beta V_t}$ $g_{\pi} = 0$

 g_{π} is the reciprocal of the input impedance

 g_{π} of a MOSFET is much smaller than that of a BJT (and smaller is better!)



Assume BJT operating in FA region, MOSFET operating in Saturation Assume same quiescent output voltage and same resistor R_1 One of the most widely used amplifier architectures







The functional form of the gain is the same for both circuits !



For the same power level and the same quiescent voltage drop across R_1 , the BJT will generally have a much larger gain since usually $V_t << V_{EB}$

Comparison of MOSFET and BJT



Assume BJT operating in FA region, MOSFET operating in Saturation Assume same bias current

One of the most widely used amplifier architectures in integrated applications

Special Case of Previous Architecture





- A_v is unrealistically large
- Must include more accurate small-signal model !



Functional form of gain is the same for both circuits



- BJT Gain is Very Large and Independent of Operating Point
- MOS Gain is dependent upon operating conditions (V_{EB})
- V_{AF} and 2/ λ are comparable for large MOS devices, V_{AF} considerably larger than 2/ $\lambda\,$ for short devices
- Practically, Vt<<V_{EB}
- BJT gain typically much larger than MOS gain for this configuration too

Can a single transistor be used to realize the current source?

Yes – it provides reasonable performance but there are some limitations



Current sources often characterized by their nominal output current, their small signal output impedance, and their output signal swing

Nominal output current:

$$I_{\text{OUT}} \cong \mu C_{\text{OX}} \frac{W}{2L} (V_{\text{EB}})^2$$

Can a single transistor be used to realize the current source?



Can a single transistor be used to realize the current source?

Output signal swing:



To maintain saturation region operation

$$V_{DS} > V_{GS} - V_{T}$$

 $V_{OUT} > V_{XX} - V_T$

Are there better current source circuits?

Yes – and most focus on improving either the signal swing or the output impedance

High output impedance current source:



Student Question Are there better current source circuits? I IOUT M_2 Output Impedance: M_1 $V_{\rm YY}$ $\overline{}$ V U R — i out

Are there better current source circuits? Output Impedance:





$$R_{out} = \frac{u}{i}$$

$$i = (v - v_1)g_{o2} + g_{m2}v_{gs2}$$

$$v_1(g_{o1} + g_{o2}) = g_{m2}v_{gs2} + g_{o2}v$$

$$v_1 = -v_{gs2}$$

$$R_{out} = \frac{v}{i} = \frac{g_{m2} + g_{o1} + g_{o2}}{g_{o1} + g_{o2}} \cong \left[\frac{1}{g_{o1}}\right]\frac{g_{m2}}{g_{o2}} \Longrightarrow \left[\frac{1}{g_{o1}}\right]$$